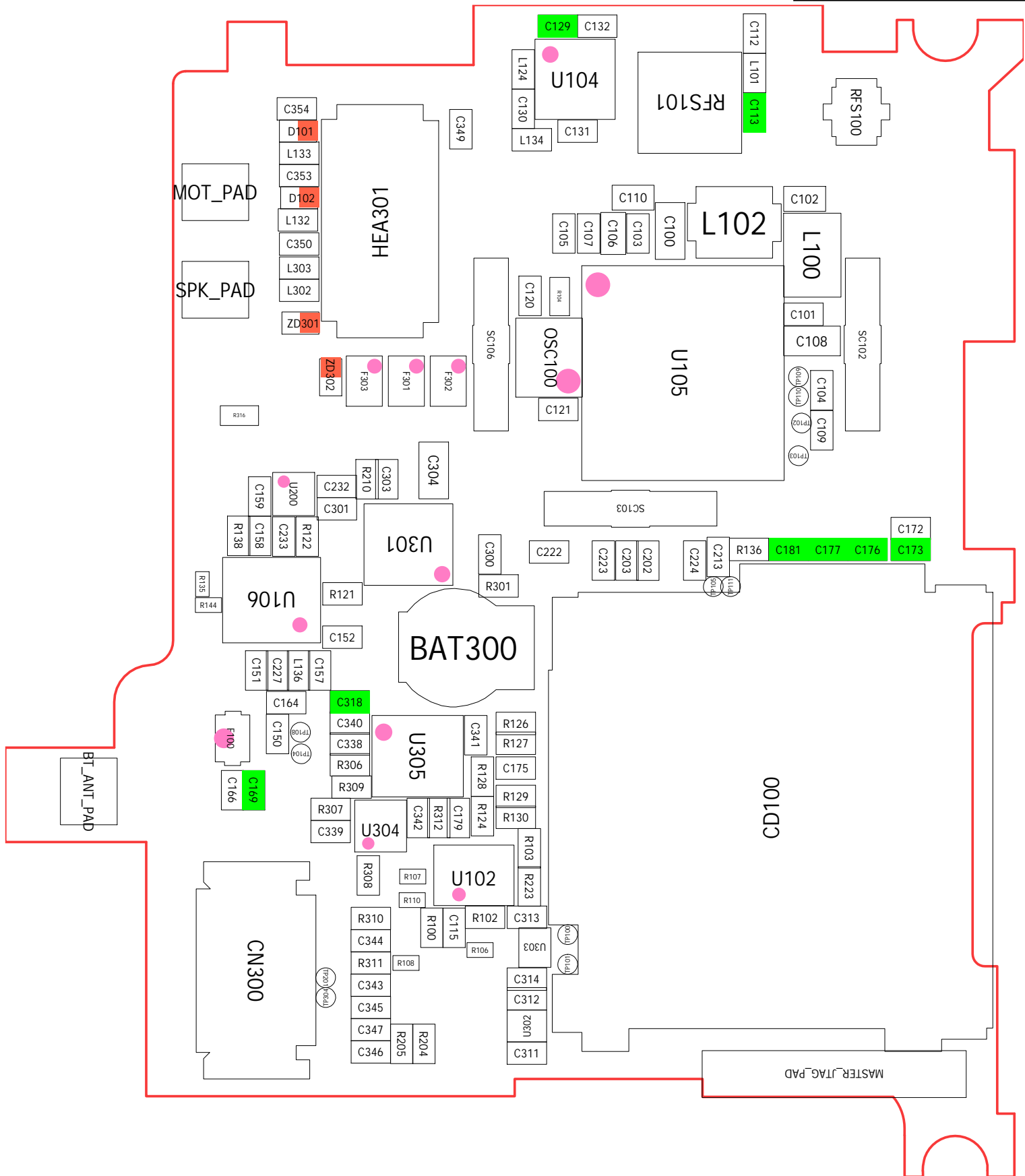


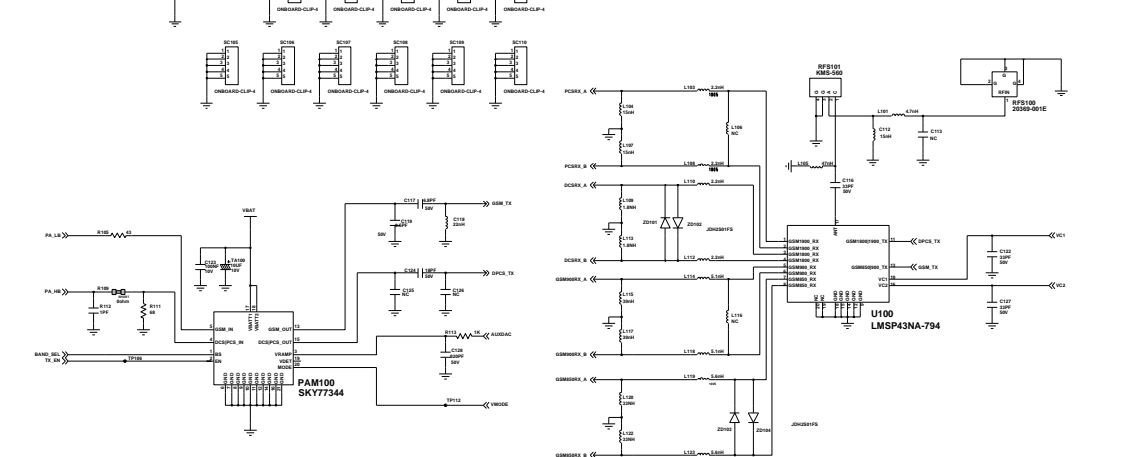
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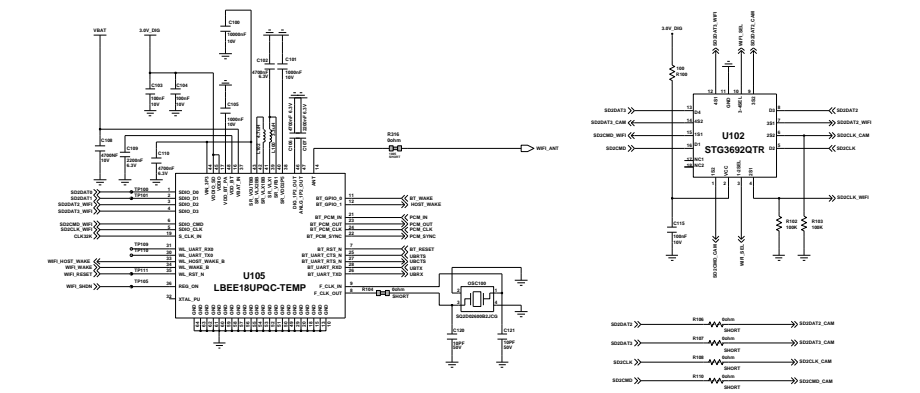


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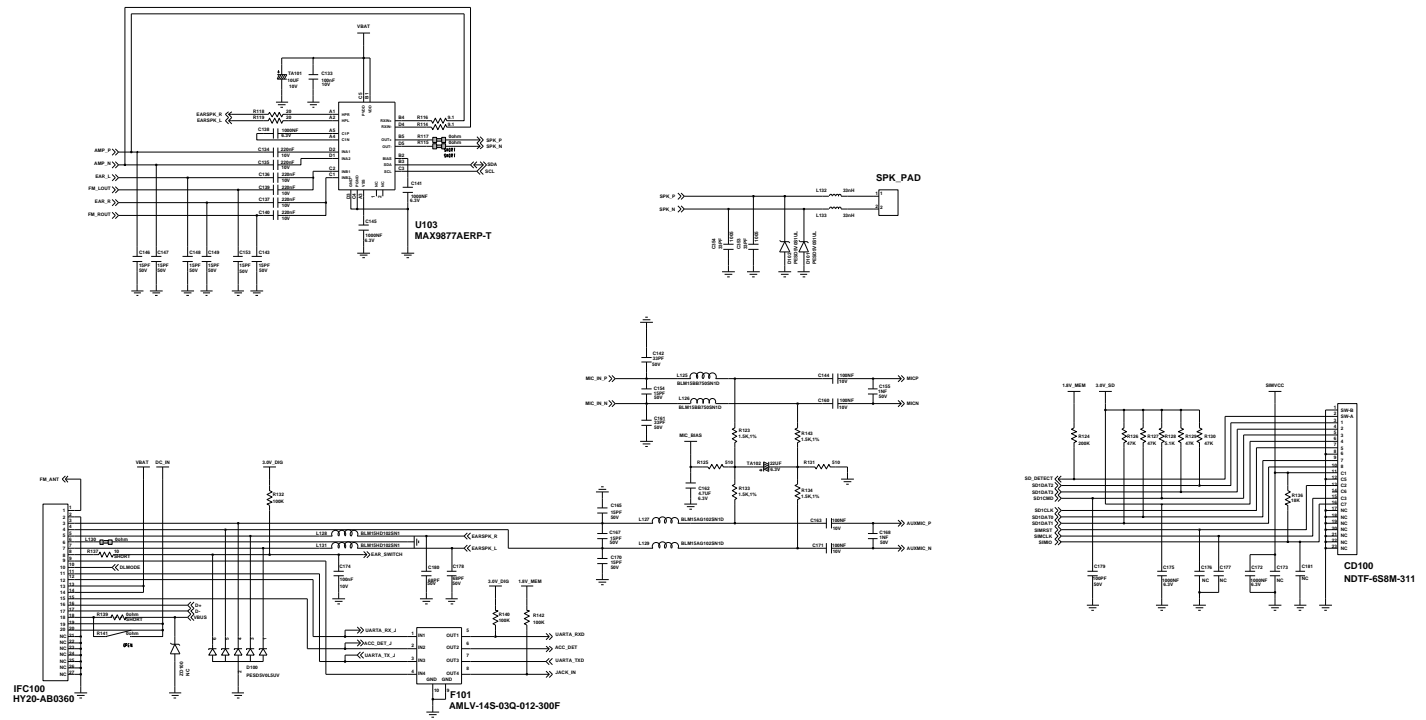


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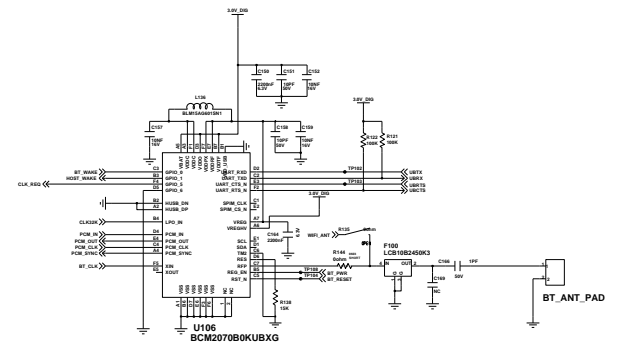
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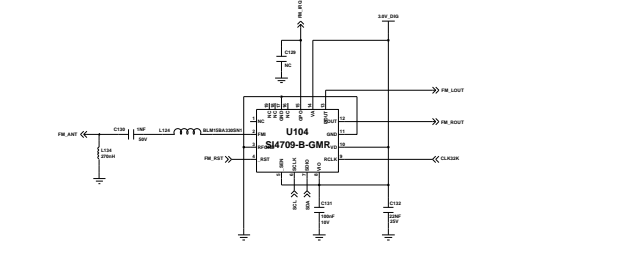
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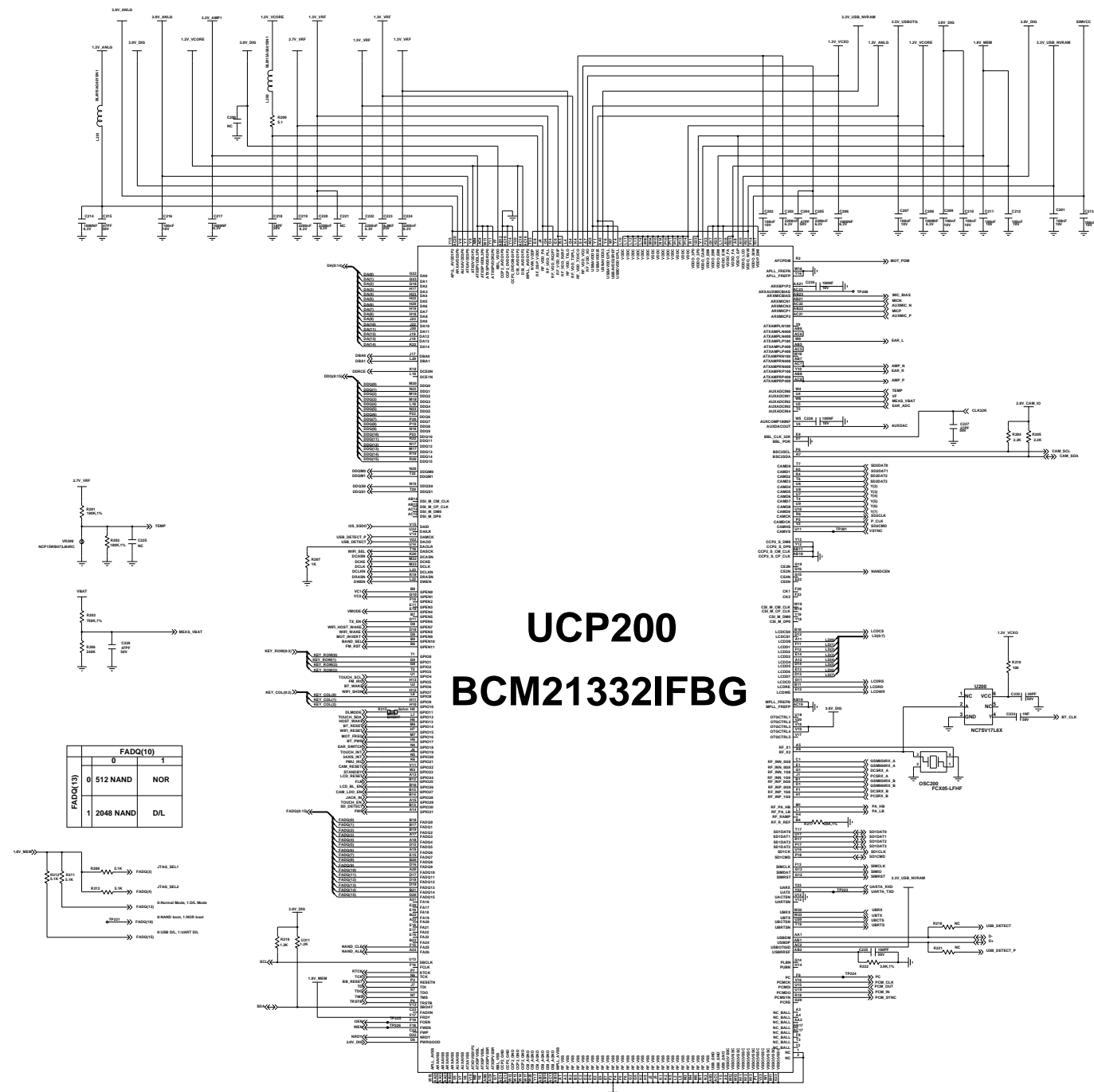


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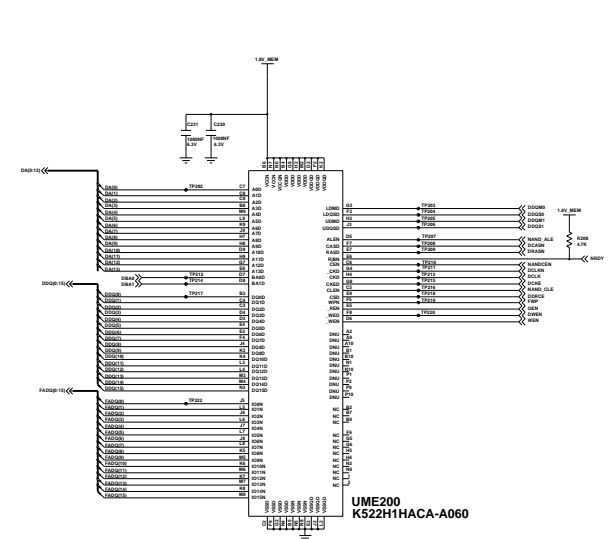
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Rev. Date	2010.03.10
Rev. Description	GT-S5230 REV0.3A
Rev. Author	RF & INTERFACE

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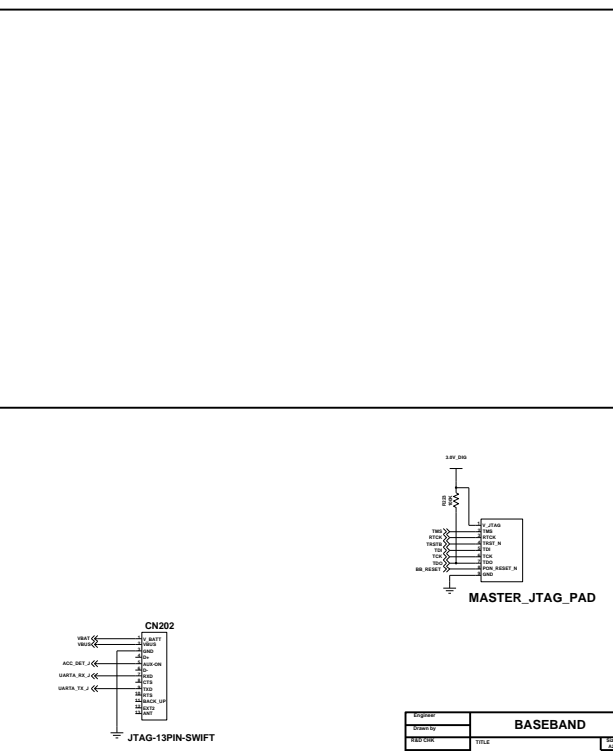


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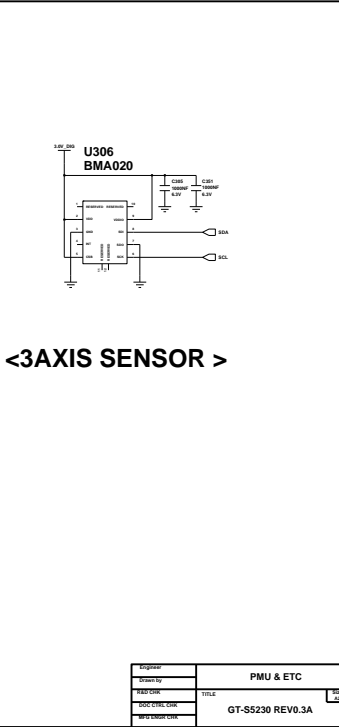
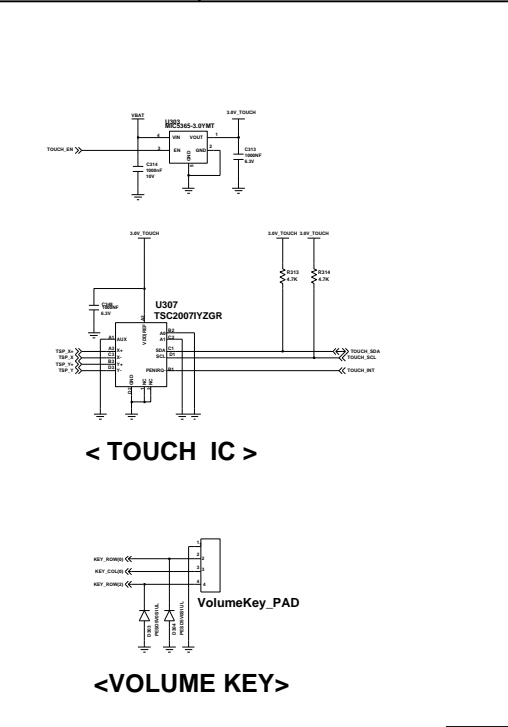
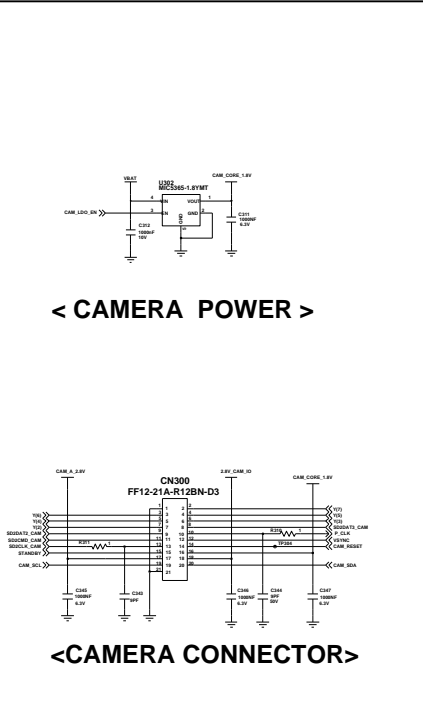
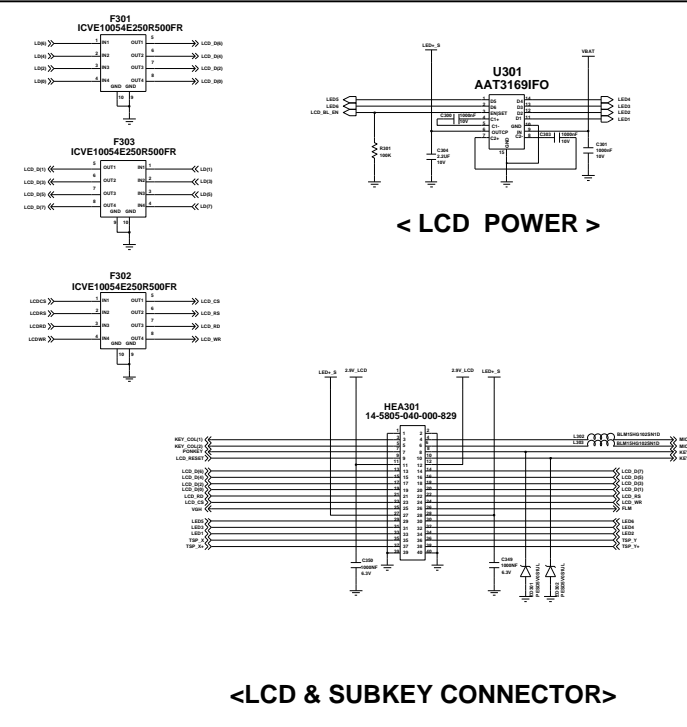
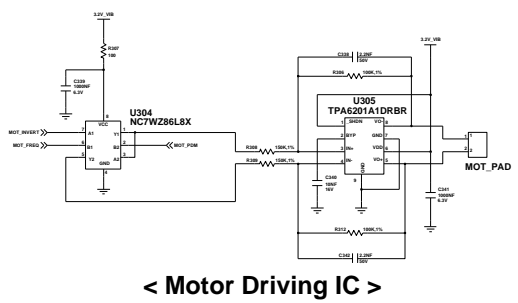
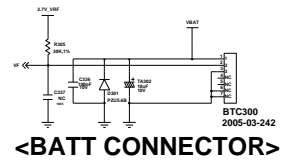
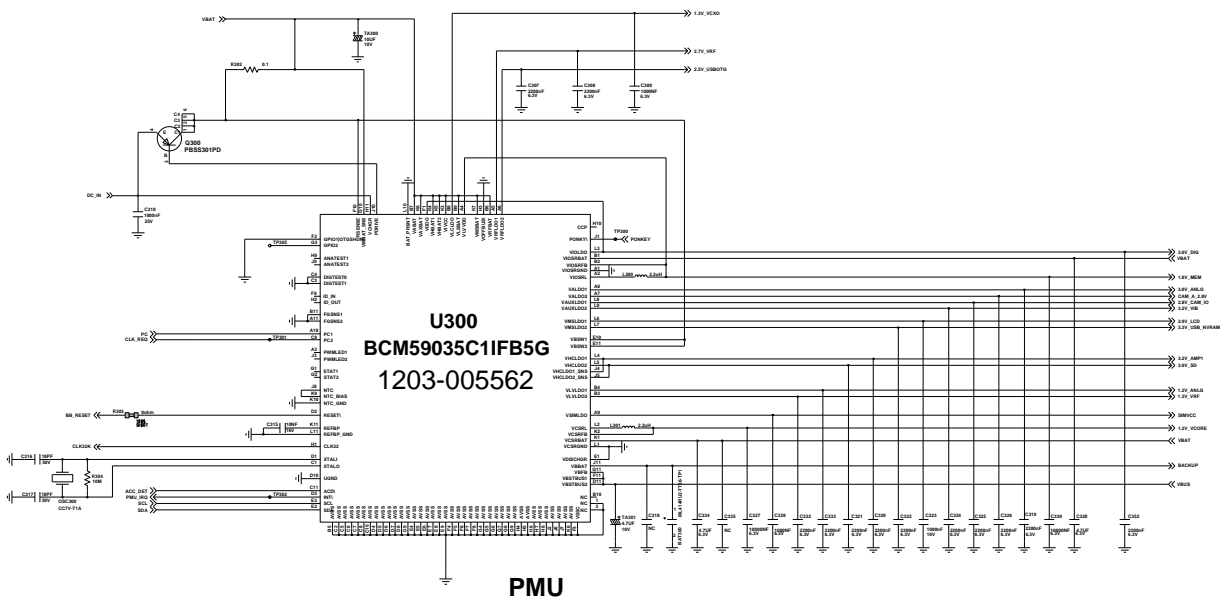
FADQ(10)	
FADQ(13)	
0	512 NAND NOR
1	2048 NAND D/L



MEMORY(Large Block NAND) (2Gb NAND + 1GB DDR SDRAM)



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Rev.	3